



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABADHYDERABAD-
500085

CHRISTU JYOTHI INSTITUTE OF TECHNOLOGY & SCIENCE(68)

B.Tech - R18 - II Year - II Semester

ELECTRONICS AND COMMUNICATION ENGINEERING

University Mid-I Internal Marks Report-Date- 2023-09-22 14.18.02

TNO	154AC	154AV	154AW	154BG	154BH
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21681A0404	9	14	9	6	14
21681A0405	9	13	12	7	14
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21681A0433	21	25	18	21	23
21681A0480	11	17	12	8	10

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Page No: 10 of 11

HTNO	154AC	154AV	154AW	154BG	154BH
21681A0434	14	17	13	9	14
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21681A0476	15	22	14	17	17
21681A0477	11	21	13	21	16
21681A0478	18	23	18	16	17

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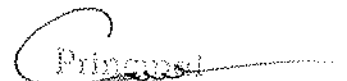
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22685A0406	8	15	8	7	14
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22685A0408	13	23	13	14	16
22685A0409	11	17	12	11	14
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22685A0413	-1	-1	-1	-1	-1
22685A0414	10	18	11	16	11
22685A0415	11	21	11	16	14

Note : '-1' Indicates Student is Absent for the exam.

Subject Code	Subject Name
154AC	ANALOG AND DIGITAL COMMUNICATIONS
154AW	ELECTRONIC CIRCUIT ANALYSIS
154BH	LINEAR IC APPLICATIONS
154AV	ELECTROMAGNETIC FIELDS AND WAVES
154BG	LAPLACE TRANSFORMS, NUMERICAL METHODS & COMPLEX VARIABLES

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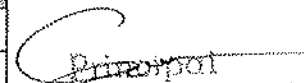
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21681A0402	21	22	17	10	23
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Jangam (Med), Jangaon (Dist)-506163

HTNO	154AC	154AV	154AW	154BG	154BH
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21681A0468	17	19	17	19	15
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21681A0477	19	22	24	14	22
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HTNO	154AC	154AV	154AW	154BG	154BH
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22685A0407	22	19	19	-1	18
22685A0408	20	22	24	17	23
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Electronics and Communication Engineering

Academic Year 2022-2023

Date: 30-12-2022

Circular

All The Students of II, III, IV Batch, II semester students are hearby informed that the Remedial classes for the students who got less than 15 marks in the 1st mid examinations are going to be held from 04-01-2023. All the students are instructed to attend the classes regularly. Timetables are displayed on the student notice board.

HOD,ECE

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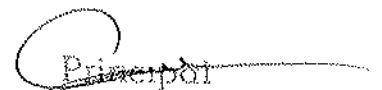
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List of The Remedial Classes for the Academic Year 2022-2023

S.No	Year-Sem-Branch	Name Of The Subject	Name Of The Teacher	No Of Students
1	II-II-ECE	Analog Digital Communications	B.Hanumanthu	55
2		Electronic Circuit Analysis	P.Thirupathi	45
3		Linear Ic Applications	R.Ramesh	25
4		Electronic Magnetic Fields	K.Harikrishna	10
6	III-II-ECE	Digital Signal Processing	Dr.Secja Mole	18
7		Fundamentals Of Management For Engineers	B.Swapna	9
8		Embedded System Design	D.Jagan	9
9		Antennas And Propagation	G.Lavanya	25
10	IV-II-ECE	Radar Systems	B.Sandeep	3
11		Low Power Vlsi Design	S.Sravya Shruthi	2
12		Database Management	P.U.Anitha	3


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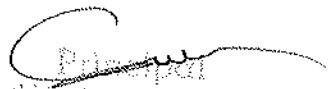
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Electronic Magnetic Field 2022-2023

REMIDIAL CLASS

Sub:AE

H-T NO	23/9	30/9	07/10	28/10	04/11	11/11	18/11	25/11
21681A0404	P	A	A	P	P	P	P	P
21681A0405	P	P	A	P	A	P	P	P
21681A0446	P	P	P	P	A	P	P	P
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Date	Topic
23/9	Coulomb's Law & Electric Field Intensity
30/9	Boundary Conditions
07/10	Poisson's And Laplace's Equations
28/10	Biot-Savart's Law & Ampere's Circuit Law
04/11	Faraday's Law & Transformer And Motional EMF
11/11	Relation Between Field Theory And Circuit Theory
18/11	Electromagnetic Wave Generation And Equations
25/11	Poynting Vector


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Linear Ic Applications

REMIDIAL CLASS

Sub:ECA

H-T NO	20/9	27/9	04/10	11/10	25/10	01/11	08/11	15/11
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21681A0404	P	A	P	P	A	P	P	P
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22685A0415	A	P	P	P	A	P	P	P


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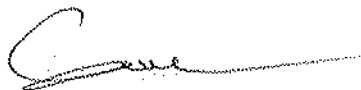
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Date	Topic
20/9	Characteristics Of OP-Amps
27/9	DC And AC Characteristics: 741 Op-Amp & Its Features
04/10	Inverting And Non-Inverting Amplifier
11/10	All Pass Filters, Oscillators
25/10	Monostable And Astable Operations
01/11	Frequency Multiplication, Frequency Translation
08/11	Counter Type ADC
15/11	DAC And ADC Specifications


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HVDC TRANSMISSION TUTORIL QUESTION

Tutorial-1

- 1) Describe in brief the different components present in HVDC systems with neat Diagram.
- 2) A bridge connected rectifier operates with $\alpha=30^\circ$ and $\mu=150^\circ$. Determine the necessary line secondary voltage of rectifier transformer which is nominally rated at 220/10KV, if it is required to obtain a dc output voltage of 100KV.

Tutorial-2

- 1) For a fixed power of transmission explain how the economic choice of voltage level is selected in D.C. transmission system.
- 2) Illustrate the operation of Graetz circuit (Inverter) and analyze the output voltage expression and draw its equivalent circuit.

Tutorial-3

- 1) Analyze the concept of DC power flow control and indicate the necessary control steps and trajectory of the operating point on the hvdc control .
- 2) Describe about the reactive power requirements of converter in steady state and describe conventional and alternate control strategies.

Tutorial-4

- 1) Description on different types of Static var compensators.
- 2) Explain about firing angle control methods in hvdc system.
 - a) Individual phase control method
 - b) Equidistant pulse control method

Tutorial-5

- 1) Evaluate the mathematical models of DC network and DC converter including converter controller..
- 2) Explain P.U system for DC quantities.

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Tutorial-6

- 1) Classify the solution methodology for AC-DC load flow and explain.
 - a) Simultaneous method.
 - b) Sequential method.
- 2) Estimate the solution of DC load flow with necessary equations.

Tutorial-7

- 1) Illustrate in detail about the protection against over currents in HVDC converters with schematic diagram.
- 2) Sketch the schematic diagram indicating the arrangement of surge arresters in a converter station. Explain how their disruptive capabilities vary with respect to the location.

Tutorial-8

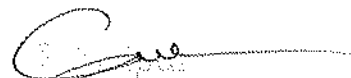
- 1) State the corona phenomenon in HVDC line. Explain in detail the effects of corona on DC line.
- 2) Describe the effect of pulse number on harmonics.

Tutorial-9

- 1) Using Fourier analysis obtain equation for primary current of transformer connected to 12-pulse converter.

Tutorial-10

- 1) Derive an equation for harmonic voltage and current for single tuned filter and discuss the influence of network admittance on design aspects. (C413.6, Understand)
- 2) Why high pass filters are provided with damping resistor? Also explain about advantages and disadvantages


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PS-II QUESTION BANK

UNITWISE SHORT ANSWER QUESTIONS

UNIT-I

1. What is short transmission line
2. Write the properties of ABCD parameters
3. What is medium transmission line
4. Prove that velocity of light is equal to travelling wave
5. Why is leakage conductance is negligible in overhead lines
6. Give the nominal PI representation of medium line.
7. Define Ferranti effect
8. Why is leakage conductance negligible in overhead lines

UNIT-II


9. Write the Reflection & Refraction coefficients of open & short circuited lines
10. Define voltage fluctuation
11. Define voltage stability
12. Give the merits and demerits of Tap changing Transformer.
13. List advantages of using a shunt capacitor for voltage control
14. Give the applications of synchronous condenser

UNIT-III

15. Write the equation for per unit impedance if change of base occurs.
16. How are the loads represented in the reactance and impedance diagram.
17. What is attenuation
18. Explain about surge diverter?
19. Explain about functions of counter poise ?
20. What is Peterson coil? what protective functions are performed by this device.
21. what is the advantage of per unit method?

UNIT-IV

22. List the devices used for protection against lightning.
23. What is lightning arrester.
24. What about ground rods
25. What are the requisites of a good lightning arrester?
26. Explain , why the surge diverter are located very close to equipment
27. what is significance of volt-time curve in power system studies


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UNIT-V

28. Draw the zero sequence equivalent network for star/delta connected transformer.
29. What is the need for short circuit study.
30. Draw the sequence network for delta/delta connected transformer.
31. What are symmetrical components

LONG ANSWER QUESTIONS

UNIT-I

1. Derive the expression for regulation and efficiency of a medium transmission line using nominal π method. Draw phasor diagram also.
2. Input to a single-phase short line is 2000 KW at 0.8 pf lagging. The line has a series impedance of $(0.4 + j0.4)$ ohms. If the load voltage is 3 KV, find the load and receiving end power factor. Also find supply voltage and supply power factor.
3. Discuss why equivalent π circuit of a long line is preferred over the equivalent T circuit.
4. A three phase 50 Hz transmission line is 150 km long and delivers 25 MW at 0.85 power factor lagging and at 110 KV. The resistance and reactance of the line per conductor per km are 0.3 ohms and 0.9 ohms respectively. The line charging admittance is 0.3×10^{-6} mho per km per phase. Compute by applying the nominal π method the voltage regulation and transmission efficiency.
5. Derive the expression for regulation and efficiency of a medium transmission line using nominal π method. Draw phasor diagram also.
6. b) Input to a single-phase short line is 2000 KW at 0.8 pf lagging. The line has a series impedance of $(0.4 + j0.4)$ ohms. If the load voltage is 3 KV, find the load and receiving end power factor. Also find supply voltage and supply power factor.
7. Derive equivalent π circuit of a long line
8. A three phase 50 Hz transmission line is 150 km long and delivers 25 MW at 0.85 power factor lagging and at 110 KV. The resistance and reactance of the line per conductor per km are 0.3 ohms and 0.9 ohms respectively. The line charging admittance is 0.3×10^{-6} mho per km per phase. Compute by applying the nominal T method the voltage regulation and transmission efficiency
9. Explain clearly the 'Ferranti effect' with a phasor diagram
10. A 3-phase 50 Hz transmission line has resistance, inductance and capacitance per phase of 10 ohm, 0.1 H and 0.9 μ F respectively and delivers a load of 35 MW at 132 kV and 0.8 p.f. lag. Determine the efficiency and regulation of the line using (i) nominal-T, (ii) nominal- π .
11. Derive the ABCD parameters of a nominal π represented medium length transmission line with

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neat phasor diagram

12. Classify the transmission lines
13. Explain the Ferranti effect with a phasor diagram and its causes.
14. Explain the classification of lines based on their length of transmission
15. What are the factors which govern the performance of a transmission line?
16. What is an equivalent π circuit of long line? Derive expression for parameters of this circuit in terms of line parameters.

17. UNIT-II

1. Explain the working of on-load tap changing transformer for voltage control.
2. A 3-phase line has an impedance of $(20 + j60)$ ohm per phase. The sending end voltage is 142 kV while the receiving end voltage is maintained at 132 kV for all loads by an automatic phase modifier. If the kVAR of the modifier has the same value for zero load as for a load of 50 MW, determine the rating of the modifier and the p.f. of this load.
3. Explain series and shunt compensation of lines and discuss their effect on the surge impedance loading of the lines. If shunt compensation is 100%, what happens to SIL and voltage profile?
4. A radial long uncompensated line with constant sending end voltage is terminated through an asynchronous load, derive an expression for maximum power transfer when termination is through a variable resistance. Hence discuss the voltage instability problem.
5. Explain the working of on-load tap changing transformer for voltage control
6. A 3-phase line has an impedance of $(40 + j80)$ ohm per phase. The sending end voltage is 162 kV while the receiving end voltage is maintained at 400 kV for all loads by an automatic phase modifier. If the kVAR of the modifier has the same value for zero load as for a load of 70 MW, determine the rating of the modifier and the p.f. of this load.
7. How do you determine the capacity of the phase modifier if the net reactive power required to maintain certain voltages at the two ends is known? Explain
8. What is the need of compensation in power system? Explain about Load ability characteristics of overhead lines
9. Explain the surge impedance loading with necessary expressions
10. What is the need of compensation in power system? Explain about Load ability characteristics of overhead lines
11. What is difference between compensated and uncompensated transmission line?
12. Describe about radial line with asynchronous load

UNIT-III

1. Explain the p.u. system of analysing power system problems. Discuss the advantages of this method over the absolute method of analysis.
2. Two generators rated at 10 MVA, 13.2 kV and 15 MVA, 13.2 kV are connected in parallel to a busbar. They feed supply to two motors of inputs 8 MVA and 12 MVA respectively. The operating voltage of motors is 12.5 kV. Assuming base quantities as

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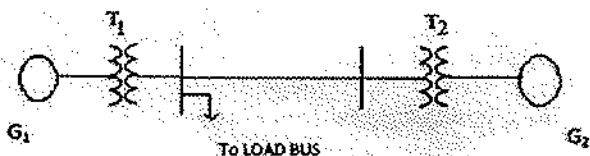
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3. 50 MVA and 13.8 Kv, draw the reactance diagram. The percent reactance for generators is 15% and that for motors is 20%.
4. Using Bewley's Lattice diagram, represent the voltage and current waveforms of a short-circuited line.
5. An overhead line with surge impedance 400 ohms bifurcates into two lines of surge impedance 400 ohms and 40 ohms respectively. If a surge of 20 kV is incident on the overhead line, determine the magnitudes of voltage and current which enter the bifurcated lines.
6. Explain series and shunt compensation of lines and discuss their effect on the surge impedance loading of the lines. If shunt compensation is 100%, what happens to SIL and voltage profile?
7. A radial long uncompensated line with constant sending end voltage is terminated through an asynchronous load, derive an expression for maximum power transfer when termination is through a variable resistance. Hence discuss the voltage instability problem
8. Discuss the advantages of p.u. system method over the absolute method of analysis.
9. Show that a travelling wave moves with a velocity of light on the overhead line and its speed is proportional to $1/\epsilon_r$ on a cable with dielectric material of permittivity ϵ_r
10. Describe about Attenuation of travelling waves.
11. Determine the equations for the reflection and refraction coefficients for a short circuited line
12. A surge of 200 KV travelling on a line of surge impedance 400Ω reaches a junction of the line with two branch lines of surge impedance of 500Ω and 300Ω respectively. Find the surge voltage and current transmitted into each branch line.
13. State the advantages of p. u system
14. Draw the impedance diagram for the electric power system shown in figure showing all impedances in per unit on a 100 MVA base. Choose 12 kV as the voltage base for generator. Three phase power and line ratings are as below:
G1:90MVA, 12kV, X=9% T1:80MVA,
12/220kV, X=16% T2:80MVA,
220/7.2kV, X=20% G2:90MVA, 7.2kV,
X=9% Line:220kV, X=120 Ω
Load Bus:220kV,S=48MW+j64MVAr



UNIT-IV

1. Describe the construction, principle of operation and applications of

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- a. Expulsion gap; and
 - b. Valve type lightning arrester
2. What is volt-time curves? What is their significance in power system studies?
 3. What are ground rods and counterpoises? Explain clearly how these can be used to improve the grounding conditions. Give various arrangements of counterpoise.
 4. What is insulation coordination and describe its significance in selection of protective equipment? Explain with volt-time characteristics
 5. Describe the construction, principle of operation and applications of
 - a. Horn gap and b. rod gap lightning arrester
 6. Explain a. counter poise b. Grounds c. neutral earthing

UNIT-V

1. Derive an expression for the fault current for a double line to ground fault as an unloaded generator and draw its equivalent circuit.
2. A generator rated 120MVA, 11kV has $X_1 = X_2 = 30\%$ and $X_0 = 15\%$. Its neutral is grounded through reactance of 0.1Ω . The generator is operating at rated voltage, load is disconnected from the system when double line to ground fault occurs at its terminals. Find the sub-transient current in the faulted phases and line to line fault current.
3. Obtain the symmetrical components of the following set of unbalanced currents $I_a = 1.6 \angle 250^\circ$, $I_b = 1.0 \angle 180^\circ$ and $I_c = 0.9 \angle 132^\circ$. Also find out the neutral current
4. Derive an expression for the fault current for a double line to ground fault as an unloaded generator and draw its equivalent circuit.
5. Find symmetrical components for the given three phase voltages:
 $V_a = 300 \angle -120^\circ$, $V_b = 200 \angle 90^\circ$ and $V_c = 100 \angle -30^\circ$
6. Develop the connection diagram of sequence network when a line to line fault occurs in a power network.

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LIST OF AWARDS

Criteria for Selection of Academic Merit Award:

One student each from all branches, who secure the highest CGPA in a minimum period & in the first attempt in the regular end examinations will be awarded a gold medal and from amongst these branch toppers, one student will be given a gold medal for being college over-all topper

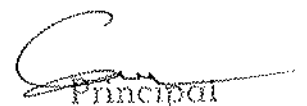
List of Awardees for the Academic Year 2022-2023

Academic Merit Award

Sl.No	Branch	Name of the Student	CGPA
1	Civil Engineering	Endunoori Swarnalatha	7.9
2	Electrical & Electronics Engineering	Mothe Srujan	8.63
3	Electronics & Communication Engineering	Farheen	8.93
4	Computer Science Engineering	Chinnoju Udaya Sree	8.2

Overall College Topper

Sl.No	Branch	Name of the Student	CGPA
1	Electronics & Communication Engineering	Farheen	8.93


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Department Of Mechanical Engineering

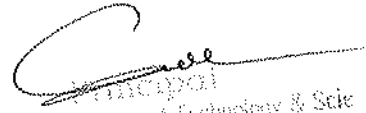
Academic Year 2022-2023

Date : 05-09-2022

Circular

All the final year students are hereby informed that the department is planning to organize GATE classes in online mode via Google meet. Interested students should register with the academic coordinator of our branch MR.Dr.yakoob on or before 07-09-2022


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Department of Mechanical Engineering


Academic Year 2022-2023

Date: 11/09/2022

NOTICE

It is hereby informed that the following is the list of faculties allotted for GATE classes. All students should attend the classes online (Google meet) without fail and avail the opportunity

SI NO	NAME OF THE SUBJECT	NAME OF THE FACUTY
1	Engineering Mathematics	G.Radhika
2	Engineering Mechanics	Santhoshi Kumari
3	Theory of Machines	S.Sudheer
4	Fluid Mechanics	Ch.Sunil
5	Heat-Transfer:	K.Karunaker
6	Thermodynamics:	D.Venkata Ramana
7	Engineering Materials	K.Madhu
8	Production Planning and Control	G.sunil
9	Machining and Machine Tool Operations	K.Yakoob


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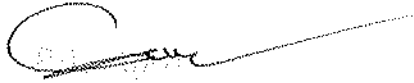
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Details of membership	Details of subscription	Name of service subscribed	No E resources with full text access	Whether remote access provided (YES/NO)
DELNET Membership	IM-9174	-	135	YES
National digital library		National Digital library	-	YES

Screen Shots of E-Resources

S.No	Name of E-Resources available
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2	National digital library


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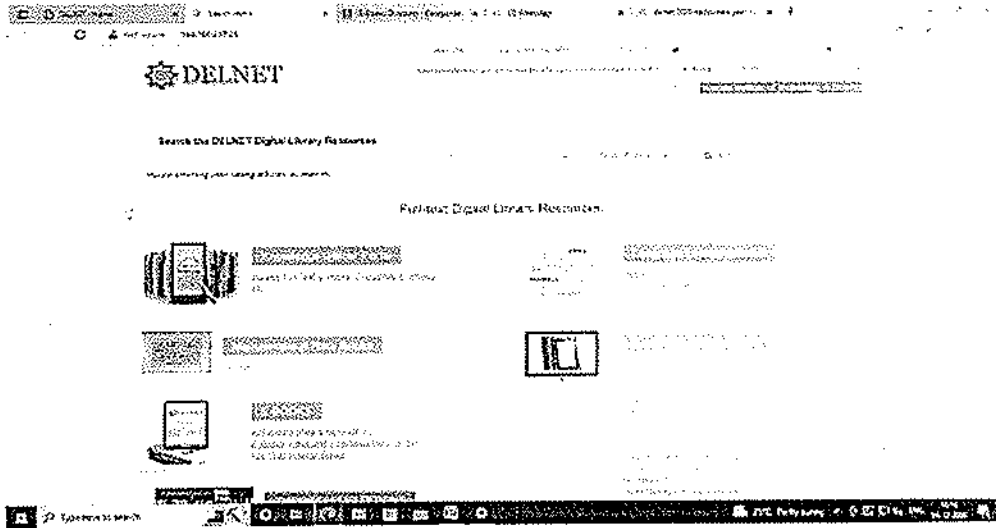
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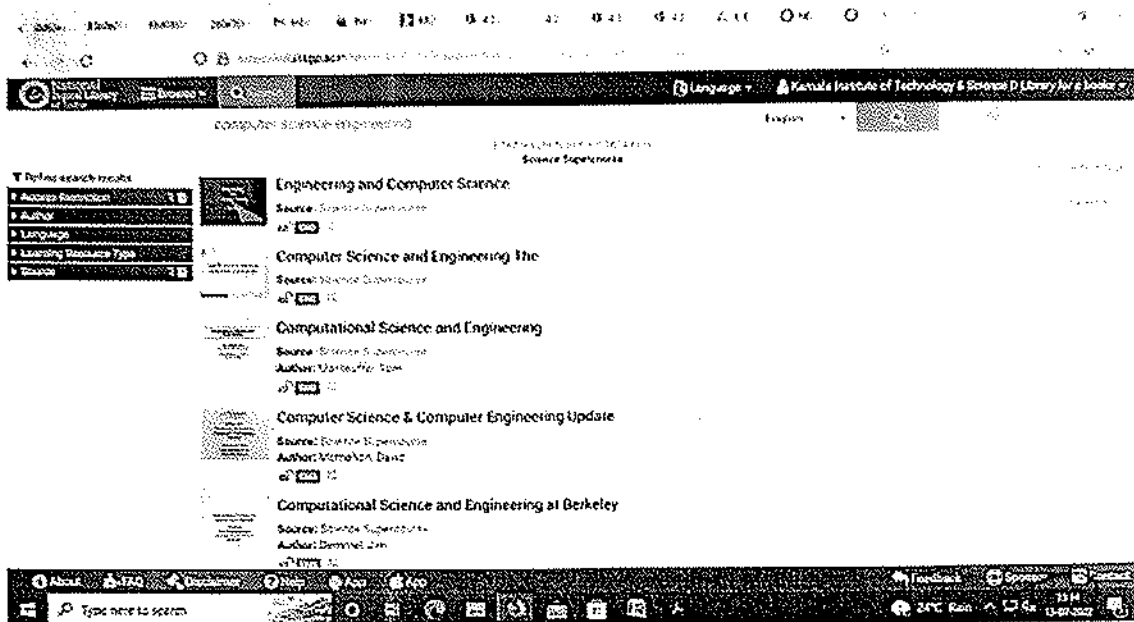
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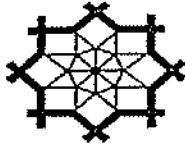


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Date of Issue: June 6, 2023

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Director
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Department of computer science Engineering

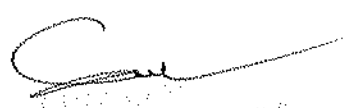
Project List 2022-2023

Batch No	Major Project Title	Roll No	PO Mapping	PSO Mapping
1	Design of Advanced Multiplier using 15X4 Compressor	18681A0409	1,2,3,4,5,6,9,10,11,12	1,2, 3
		19685A0409		
		18681A0433		
		17J91A0409		
2	Design and implementation of Power Optimal reversible FIR Filter using Wallace tree Multiplier	18681A0421	1,2,3,4,5,6,9,10,11,12	1,2, 3
		18681A0435		
		18681A0402		
		18681A0436		
3	Implementation of AES using Composite field arithmetic field for IOT applications	18681A0401	1,2,3,4,5,6,9,10,11,12	1,2, 3
		18681A0422		
		19685A0412		
		17681A0401		
4	Design of Wallace tree using low power compressors	18681A0429	1,2,3,4,5,6,9,10,11,12	1,2, 3
		18681A0418		
		19685A0410		
		19685A0415		
5	Design of efficient reversible ALU using reversible logic gates	18681A0411	1,2,3,4,5,6,9,10,11,12	1,2, 3
		19685A0405		
		18681A0419		
		17681A0452		
6	Efficient design of reversible Sorting circuit using nano technology	18681A0426	1,2,3,4,5,6,9,10,11,12	1,2, 3
		18681A0406		
		19685A0418		
		18681A0434		
7	Design and implementation of reversible convolution using DADDA multiplier	18681A0424	1,2,3,4,5,6,9,10,11,12	1,2, 3
		18681A0430		
		16681A0442		
		18681A0417		
8	Modified high speed 32-bit Vedic multiplier Design and implementation.	18681A0412	1,2,3,4,5,6,9,10,11,12	1,2, 3
		18681A0427		
		19685A0401		
		17681A0		
		19685A0402		

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9	VLSI implementation of modified Boolean Algebra using Cryptographic Algorithm	18681A0428	1,2,3,4,5,6,9,10,1 1,12	1,2, 3
		18681A0404		
		19685A0408		
		17681A0432		
10	Design of Data encoding Techniques for reducing energy consumption in network on chip	18681A0405	1,2,3,4,5,6,9,10,1 1,12	1,2, 3
		18681A0414		
		18681A0416		
		18681A0408		
11	Design and implementation of MIMO using QPSK in wireless communication	19685A0406	1,2,3,4,5,6,9,10,1 1,12	1,2, 3
		19685A0403		
		18681A0403		
		18681A0431		
12	Double MAC on DSP :Boosting the performance of convolutional Neural networks	18681A0407	1,2,3,4,5,6,9,10,1 1,12	1,2, 3
		19685A0404		
		19685A0419		
		17681A0411		
13	Design of Candy Vending machine using VHDL	18681A0410	1,2,3,4,5,6,9,10,1 1,12	1,2, 3
		19685A0411		
		17J91A0411		
		18681A0423		
14	Design and implementation of density based traffic control system using Verilog.	19685A0413	1,2,3,4,5,6,9,10,1 1,12	1,2, 3
		19685A0407		
		19685A0414		
		18681A0432		
		16681A0443		




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DATE: 03/10/2022

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// Attention for final year B.Tech students//

B.Tech IV year Electrical And Communication Engineering TASK registered students are informed to attend POP sessions (soft skills, Resume preparation, Interviews and Group discussions), Training from 4th and 5th October 2022, without fail

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Students 2022-2023

S.No	Roll Number	Name	Branch	Day-1	Day-2
1	19681A0411	Kandukuri Keerthi	EEE	P	P
2	19681A0412	Kandukuri Siri	EEE	P	P
3	19681A0427	Sreeramoju Ramya Sri	EEE	P	P
4	19681A0401	Ayla Sahithya	EEE	P	P
5	19681A0402	Bandi Divya	EEE	P	P
6	19681A0406	Chadagonda Akhila	EEE	P	P
7	19681A0408	Elasagaram Kavya	EEE	P	P
8	19681A0409	Gadaraju Harika	EEE	P	P
9	19681A0410	Guttamidi Shirisha	EEE	P	P
10	19681A0415	Mandalaju Shiva Kumar	EEE	P	P
11	19681A0416	Manguru Devendar	EEE	P	P
12	19681A0420	Nariga Sathwika	EEE	P	P
13	19681A0422	Noorjaha	EEE	P	P
14	19681A0424	Pendem Anusha	EEE	P	P
15	19681A0426	Samudrala Roja	EEE	P	P
16	19681A0432	Vascema Begum	EEE	P	P
17	19681A0433	Ayla Dhanush Chary	EEE	P	P
18	19681A0434	Kasarla Vinaya Ragini	EEE	P	P
19	19681A0437	Balaboina Rajini	EEE	P	P
20	19681A0438	Cheguri Rekha	EEE	P	P
21	19681A0439	Rondla Supriya	EEE	P	P



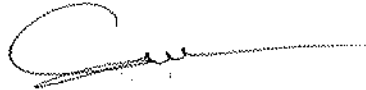
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22	19681A0440	Amaragani Swetha	EEE	P	P
23	19681A0441	Bandi Anusha Goud	EEE	P	P
24	19681A0442	Maheshwaram Karthik	EEE	P	P
25	19681A0443	Pasham Swapna	EEE	P	P
26	19681A0444	Chinthalapudi Vinay Kumar	EEE	P	P
27	19681A0445	Ponna Sujatha	EEE	P	P
28	19681A0446	Puli Prasanna	EEE	P	P
29	20685A0406	Baskula Lilora	EEE	P	P


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DATE: 03/10/2022

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B.Tech IV Year MECHANICAL ENGINEERING TASK Registerd Students Are Informed To Attend POP Sessions (Soft Skills, Resume Preparation, Interviews And Group Discussions), Training From 4th And 5th October 2022,Without Fail

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Christu Jyothi Institute of Technology & Science
Colombo Nagar, Yeshwanthapuram (Vr.)
Jangaon(Mdi), Jangaon (Dist)-506167



CHRISTU JYOTHI INSTITUTE OF TECHNOLOGY & SCIENCE

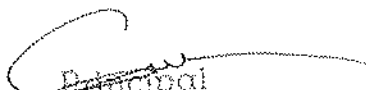
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NBA Accredited Programme, Affiliated to AICTE & JNTU-Hyd

Students 2022-2023

S.No	Roll Number	Name	Branch	Day-1	Day-2
1	19681A0304	Gurram Kalyankumar	Mech	P	P
2	19681A0311	Bir Mota Vimal	Mech	P	P
3	20685A0305	Gangadari Shiva Prasad	Mech	P	P
4	20685A0310	Digoju Sreenathchary	Mech	P	P
5	20685A0311	Vanam Vinay Kumar	Mech	P	P
6	20685A0312	Akquera Akshay	Mech	P	P
7	20685A0313	Vasam Shiva	Mech	P	P
8	20685A0314	Singa Mallikarjun Raju	Mech	P	P
9	20685A0315	Ajmeera Srikanth	Mech	P	P


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